

Rec'd PTO 18 DEC 2001  
09/936 041

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Application No.: 09/936,041

Filed: September 7, 2001

Docket No.: 110553

For: METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR

SUPPLEMENTAL PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

As a supplement to the preliminary amendment filed on September 7, 2001, and prior to initial examination, please further amend the above-identified application as follows:

IN THE ABSTRACT:

Please replace the Abstract filed with the substitute Abstract attached hereto.

IN THE SPECIFICATION:

Please replace the specification with the substitute specification attached hereto.

IN THE CLAIMS:

Please replace claims 1-13 as follows:

1. (Amended) A method of manufacturing a thin-film transistor, comprising:

forming a channel region facing a gate electrode through a gate insulating

*Bulk gate  
cont  
film, cont*